

1. A display device comprising a first substrate with a conductor pattern, parts of which define pixels, wherein at least within a viewing area of the display device, the conductor pattern, viewed transversely to the substrate along a direction from the conductor pattern toward the substrate, substantially covers the corresponding part of the first substrate, and wherein the parts of the conductor pattern are substantially mutually separated by partitioning paths having a minimal path width.

3. A display device as claimed in claim 1, characterized in that the partitioning paths have a substantially constant width.

4. A display device as claimed in claim 2 or 3, characterized in that the partitioning paths at least locally have a curved course.

5. A display device as claimed in claim 2 or 3, characterized in that at least 80% of the partitioning paths has a minimal path width.

6. A display device as claimed in claim 1, characterized in that it comprises a light-emitting material between two conductor patterns, at least one of which, viewed transversely to the substrate, substantially completely covers the corresponding part of the first substrate.

7. (Amended) A display device as claimed in claim 1, further comprising a

second substrate and a layer of electro-optical material between two conductor patterns on the first and second substrates, at least one of which conductor patterns, viewed transversely to the corresponding substrate along a direction from the one conductor pattern toward the corresponding substrate, substantially covers the corresponding substrate.

8. The display device of claim 1, wherein the conductor pattern is transparent.

9. (Amended) The display device of claim 1, wherein a distance between adjacent parts of the conductor pattern is substantially constant.

10. (Amended) A display device comprising:  
first and second substrates separated and confronting each other,  
a first conductor pattern on a side of the first substrate nearest the second substrate, the first conductor pattern defining pixels of the display device, and  
a second conductor pattern on a side of the second substrate nearest the first substrate,

wherein, within a viewing area of the device, the first conductor pattern substantially completely covers the first substrate, and

wherein, within the viewing area of the display device, the second conductor pattern substantially completely covers the second substrate.

11. The display device of claim 10, wherein the first conductor pattern comprises a plurality of first electrodes separated from each other by a first partitioning path, and wherein the second conductor pattern comprises a plurality of second electrodes separated from each other by a second partitioning path.

12. The display device of claim 11, wherein the first and second partitioning paths, viewed along a direction perpendicular to the substrate, are substantially aligned within the viewing area of the display device.

13. The display device of claim 11, wherein each of the first and second partitioning paths has a minimal path width along at least 80% of a length thereof.

14. The display device of claim 10, further comprising an electro-optical material disposed between the first and second substrates.

15. (Amended) A display device, comprising:  
a substrate;  
a first conductor pattern disposed on the substrate, the first conductor pattern defining pixels of the display device;  
an electroluminescent material disposed on the first conductor pattern;  
a second conductor pattern disposed on the electroluminescent material,  
wherein within the viewing area of the display device, the first and second